



FBIS Architectural Design Options 14.08.2017





Goal: Collect and discuss main architectural design options for FBIS

Sensor Side Interface

Input Collection and Aggregation











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Sensor Side Interface

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#### Sensor Side Interface



# FBIS Network - Dedicated FBIS Network

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Pro	Con
P104 Very scalable approach	C92 Latency might be difficult/impossible to «forecast»



C94 Every node might introduce latency

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### **FBIS Network**





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- It is expected that today's state of the art technology has low enough latencies such that they can be neglected (for diagnostic purposes). However, this has to be confirmed (↓C92)
- Use a separate dedicated (low latency) interface to request a beam-switch-off

#### **Actuation Systems Control**

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#### **Actuation Systems Control**





- Failures of Actuation Systems and Controllers shall be detected and lead to the protected state
- If possible: The same hardware framework as for Interface to Sensor Systems

#### **Global Beam Permit Generation**

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#### **Global Beam Permit Generation**

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- Make the Protection Line bi-directional
- Make the Protection Line redundant
- Foresee a degraded mode (operation with one one line)
- Latency introduced by a Repeater is considered to be low
- Realize the Repeater such that:
  - it can be «attached» to different FBIS elements
  - other BIS Parts have direct access to the Protection Line

## Input Collection and Aggregation

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### Input Collection and Aggregation





## Input Collection and Aggregation

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#### Conclusion

#### • Signal collection and aggregation to high-speed serial link





- Serializer shall be fully redundant
- It is expected that latency introduced by serialization is low enough (to be confirmed)
- One Serlializer shall feature multiple serial links
- Serializer shall be realized such that it can read <u>and</u> generate signals
- Serializer firmware shall be generic for all Serializers (if possible)



### FBIS Logic - Fully Serialized Dual Board 1002

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### **FBIS Logic**



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#### Conclusion

Follow the «Fully Serialized Dual Board 1002» pattern





- Both boards shall redundantly compute decision logic
- Cross-verification between the boards
- Foresee a Degraded Mode (operation with a single board only)

#### Sensor Side Interface

- Discrete Interface
  - 3-Wire Current Loop Interface
  - 2-Wire Differential RS-422 with Error-Detection
  - Optical Transmission
- Data Link Interface
  - Ethernet
  - RS-422
  - High-Speed Serial Link



Input Collection and Aggregation	
FBIS Logic	
Global Beam	FBIS
Permit Generation	Network
Actuation Systems	1
Control	

Sensor Side Interface

#### Sensor Side Interface





#### Conclusion

- Different types of interfaces shall be supported
- For Discrete Signals:
  - RS422 with Error Detection
- For Data Links:
  - Network with low-level protocol
- Galvanic Isolation shall be foreseen

Sensor Side Interface			
Input Collection and Aggregation			
FBIS Logic			
Global Beam Permit Generation	FBIS Network		
Actuation Systems Control			





For more information see document:

#### FBIS\_Architectural\_Design\_Options

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